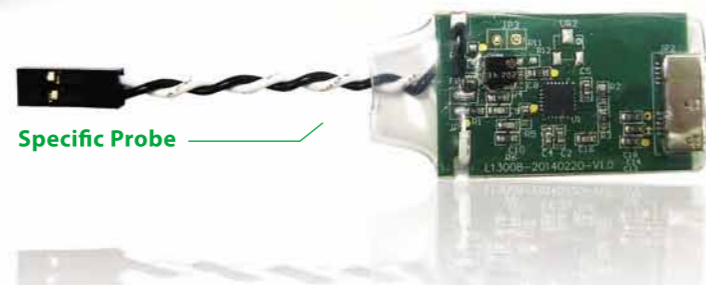


Bus Expert



Support DUT:

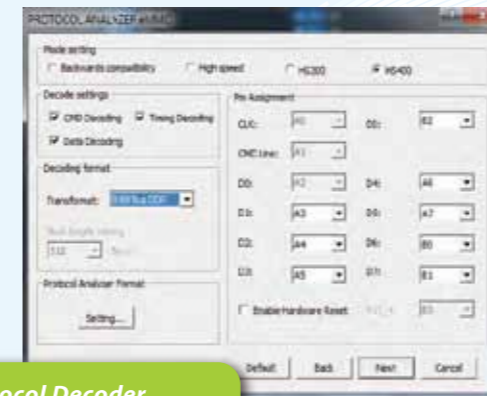
eMMC 4.51	eMMC 5.0 eMMC 5.1	SD 3.0
1 / 4 / 8 bit HS and HS200	CMD / HS400	1 / 4 bit UHS-I (up to 208MHz)



- SD3.0 eMMC5.1
- 1.6 GHz Sampling
- 200 MHz Trigger
- Emulated Calibration
- Easily Analyze Timing & Packet

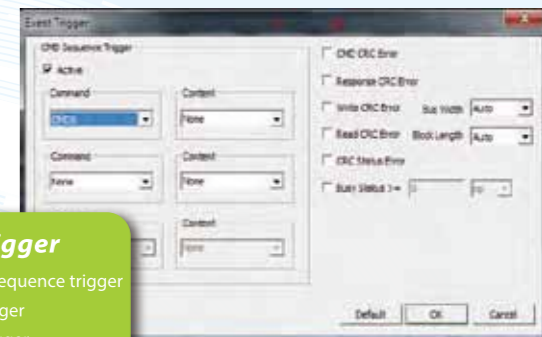
Advantages

- The newest measurement solution specific for eMMC5.1/SD3.0 NAND Flash interface.
- Capable of storing up to 250M records (hrs to days)
- Full Logic / Protocol Analyzer function fine integrated for design verification.
- Continuing upgrade auto-test items by JEDEC's up-to date releases.
- Specific designed probe & Customized FPC test fixture service support.
- Verified through main controller IC vendor evaluation platforms.



Protocol Decoder

- Support up-to-date "HS400" mode
- Support NCR...timing format
- 1, 4, 8 bit data format



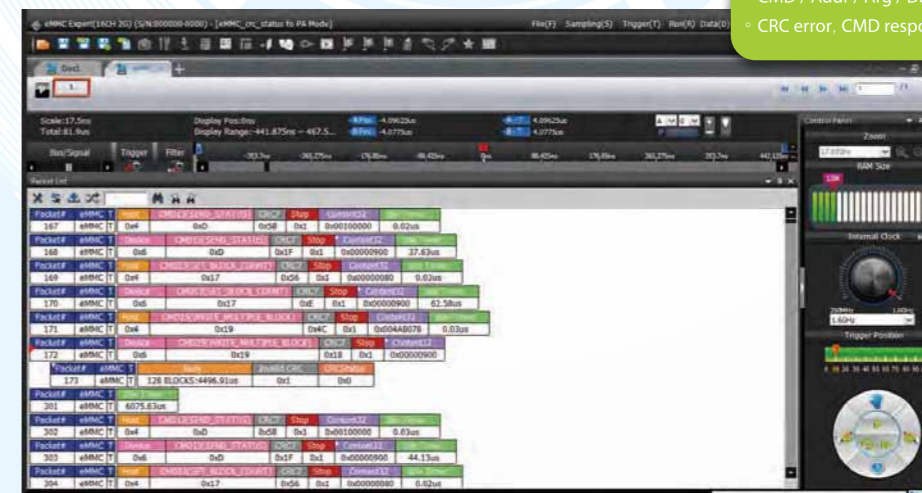
Event Trigger

- 3 level CMD sequence trigger
- CRC error trigger
- Busy Time trigger



Events log

- Hours to Days record length (250M records)
- CMD / Addr / Arg / Data / status packet name search
- CRC error, CMD response error packet content search



Procedure Auto Test

Device Identification / Data Transfer / Statistics
Read / WriteData Comparison



Emulation Calibration

78 ps resolution

